

A bit of context...

HEP is moving towards new technologies, in particular hardware accelerators



Moving from general purpose devices \Rightarrow application specific

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Monte Carlo simulation and data analysis are intensive and requires lots of computing power.

Quantum computing for HEP experiments

QC4HEP WG [arXiv: 2307.03236]

Many experimental and theoretical HEP applications are deemed to benefit from quantum computation.

Recap

- required to develop algorithms
- complete introspection
- require noise modeling

Hardware

- limited (in many senses)
- requires calibration
- final validation

Discrete gates primer

Goal: Construct a generic $U(2^n)$ operation based on building blocks

The Hilbert space on which the unitaries act is a strutured as a \bigotimes tensor product of n qubits

$$|0
angle = egin{pmatrix} 1 \ 0 \end{pmatrix} \qquad |1
angle = egin{pmatrix} 0 \ 1 \end{pmatrix}$$

the generic qubit state is:

$$\ket{\psi} = lpha \ket{0} + eta \ket{1} \qquad ext{with} \ \ |lpha|^2 + |eta|^2 = 1$$

and it can be visualized as a point on the Bloch sphere

$$lpha = \cos heta/2 \qquad eta = e^{-i\phi} \sin heta/2$$

Example gates: Pauli

X gate

The X gate acts like the classical NOT gate, it is represented by the σ_x matrix,

$$\sigma_x = egin{pmatrix} 0 & 1 \ 1 & 0 \end{pmatrix}$$

therefore

$$egin{array}{c} |0
angle \longrightarrow |1
angle \ |1
angle \longrightarrow |0
angle \end{array}$$

The Z gate flips the sign of $|1\rangle$, it is represented by the σ_z matrix,

$$\sigma_z = egin{pmatrix} 1 & 0 \ 0 & -1 \end{pmatrix}$$

therefore

 $egin{array}{c} |0
angle \longrightarrow |0
angle \ |1
angle \longrightarrow -|1
angle \end{array}$

Single-qubit gates				
These are operations on the Bloch sphere				
🖸 Two-qubit gates				
The building-block interactions				
🗓 Multi-qubit gates				
Higher-level instructions for algorithms				
Ø Define a universal gate set				
 universality means it can generate all unitarities 				
nossibly redundant since it may be efficient to execute				
- possibly reduited it, since it may be enclerit to execute				
 multiple implementations, related to diverse hardware 				

Gates could be variously parametrized, so there exists universal sets made beyond

Pauli-X (X)	- X -	$-\oplus$ -	$\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$
Pauli-Y (Y)	- Y -		$\begin{bmatrix} 0 & -i \\ i & 0 \end{bmatrix}$
Pauli-Z (Z)	— Z —		$\begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix}$
Hadamard (H)	$-\mathbf{H}$		$rac{1}{\sqrt{2}} egin{bmatrix} 1 & 1 \ 1 & -1 \end{bmatrix}$
Phase (S, P)	- S -		$\begin{bmatrix} 1 & 0 \\ 0 & i \end{bmatrix}$
$\pi/8~({ m T})$	- T -		$egin{bmatrix} 1 & 0 \ 0 & e^{i\pi/4} \end{bmatrix}$
Controlled Not (CNOT, CX)			$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$
Controlled Z (CZ)			$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{bmatrix}$
SWAP			$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$
Toffoli (CCNOT, CCX, TOFF)			$\begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 &$

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Circuit

Unitary - but measurements.

Circuit are a way to compose gates to build unitaries, *sequentially*

$$|\psi\rangle - Y - X - = -X \cdot Y - XY |\psi\rangle$$

or in **parallel**

$$\begin{array}{ccc} |\psi\rangle & - Y - Y |\psi\rangle \\ |\phi\rangle & - X - X |\phi\rangle \end{array} \Leftrightarrow \begin{array}{c} |\psi\rangle - Y \otimes X \\ |\phi\rangle & - X - X |\phi\rangle \end{array} \Rightarrow \begin{array}{c} |\psi\rangle - Y \otimes X \\ |\phi\rangle - Y \otimes X - Y \otimes X - Y \otimes X \\ |\phi\rangle - Y \otimes X - Y$$

Parametrized gate

Other parameters are possible: *GPI* and *GPI2* parametrize the position of the axis, multi-qubit gates can paramterize complex interactions, ...

Having parameters, it opens the door to optimization $\mathscr{G} \rightarrow$ i.e. quantum machine learning (QML)

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The atoms of interaction

Controlled gates (conditionals)

The controlled-\$NOT\$ (\$CNOT\$) gate is a conditional gate defined as

$$CNOT \equiv egin{pmatrix} 1 & 0 \ 0 & \sigma_x \end{pmatrix} \, ,$$

control target

|10
angle
ightarrow |11
angle

$$|00
angle
ightarrow |00
angle \qquad |01
angle
ightarrow |01
angle$$

 $|\perp \perp \rangle$

We define a control qubit which, if at |1
angle, applies X to a

target qubit.

Multi-qubit gates allow entangling states

 $\rightarrow |10\rangle$

Measurement

The non-unitary gate *that you have*

Measurements are special gates, in two ways:

- 1. it is the only operation that allows to extract information
- 2. it is the only non-unitary gate

Noise and channels

Non-unitary operations model

Instead of acting over a state vector, the state will be tracked by a density matrix

 $\ket{\psi} \longrightarrow
ho$ ($\sim \ket{\psi}ra{\psi}$)

This makes possible to track phenomena like decoherence, which has not a unitary action on the state.

Another option is to exploit measurement nonunitarity, and represent the noise through *repeated execution*. Kraus

$$\Phi(
ho) = \sum_i B_i
ho B_i^st$$

Stinespring

$$U_0 = \sum_lpha K_lpha \otimes \ket{lpha} ra{v_0}$$

Choi

- $\Lambda = |U
 angle
 angle \langle \langle U|$
- Liouville, Quantum networks, ...

Noise and channels

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Applications

credits M. Robbiati

credits M. Robbiati

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QML - remarks

A classical function being clasically optimized.

$$ar{y}_{est}(ar{ heta}) = ra{0} U(ar{ heta}) \ket{0} \quad : \quad \mathbb{R}^n o \mathbb{R}^m$$

If a first-order optimization \mathfrak{A} method used, gradient calculation may be \rightarrow "quantum-aware" (PSR).

The advantage is mainly in the inference time, and possibly ansatz expressivity.

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Quantum computation is naturally based on continuous variables. But in practice they are generated through digital control electronics with noisy calibrated pulses

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QPDF [arXiv: 2011.13934]

Ø Parametrize **Parton Distribution Functions (PDF)** with multi-qubit variational quantum circuits

Algorithm's summary:

1. Define a quantum circuit: $\mathcal{U}(\theta, x) \ket{0}^{\otimes n} = \ket{\psi(\theta, x)}$ 2. $\mathcal{U}_w(\alpha, x) = R_z(\alpha_3 \log(x) + \alpha_4) R_z(\alpha_1 \log(x) + \alpha_2)$ 3. Using $z_i(\theta, x) = \langle \psi(\theta, x) | Z_i \ket{\psi(\theta, x)}$

$$ext{qPDF}_i(x,Q_0, heta) = rac{1-z_i(heta,x)}{1+z_i(heta,x)}$$

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Density estimation with adiabatic QML [arXiv: 2303.11346]

Determining **Probability Density Functions (PDF)**

by fitting the corresponding Cumulative Density Function (CDF) using an adiabatic QML ansatz.

Algorithm's summary:

 (\mathcal{C})

- 1. Optimize the parameters $\bar{\theta}$ using adiabatic evolution: $H_{ad}(\tau; \bar{\theta}) = [1 s(\tau; \bar{\theta})]\hat{X} + s(\tau; \bar{\theta})\hat{Z}$ in order to approximate some target CDF values
- 2. Derivate from H_{ad} a circuit ${\cal C}(au;ar heta)$ whose action on the ground state of $\hat X$ returns $|\psi(au)
 angle$
- 3. The circuit at step 2 can be used to calculate the CDF
- 4. Compute the PDF by derivating ${\cal C}$ with respect to au using the Parameter Shift Rule

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Quantum hardware

Quantum computation

Various models are proposed and explored

- 1. discrete gate-based
- 2. continuous variable (a.k.a. bosonic)
- 3. quantum annealing

The potential use cases partially overlap, and it is possible to emulate each other (at least approximately).

They are particularly related to the hardware realizing them...
Technologies

Many technologies simultaneously investigated [arXiv: 2304.14360]



Pros and cons for each, investigated by different groups, including diverse private companies.

Some optimal for specific applications, others for further usage, e.g. quantum memories [arXiv: 1511.04018]

Superconducting

One of the platforms with most resonance



«IBM» and *«Google»* are definitely two prominent players, but superconducting hardware is being investigated by a plethora of labs.

Within the scope of this technology, many variations are also possible (flux-tunable qubits, couplers, cross-resonance schemes), so it is a macro-category.

Neutral atoms



«Atom computing» have been the first to claim >1000 qubits [arXiv: 2401.16177]

Control

Quantum hardware is first of all an exercise in precise control





The quantum operation is supposed to be exact, not within a certain range.

Qibo

- Your quantum workhorse -

The ecosystem





Contributors (March 2024)



Execution



Backends mechanism

Plug the framework.

Structure the integration of the various libraries.



Common operations are implemented once and reused (when possible).

Results [arXiv: 2203.08826]





Automatic differentiation

for quantum machine learning $\rightarrow Qiboml$

Autodiff simulation is fundamental to support QML investigation.

A dedicated differentiable backend in simulation can considerably help algorithms development.

Moving towards a single interface, encompassing both simulation and quantum hardware <u>implementations</u>.



Framework portability: implement in one, export derivatives.

Clifford

Specialized execution.

 $\ket{\psi} = U \ket{\psi}$

Theorem 1 Given an n-qubit state $|\psi\rangle$, the following are equivalent:

(i) $|\psi\rangle$ can be obtained from $|0\rangle \otimes n$ by CNOT, Hadamard, and phase gates only. (ii) $|\psi\rangle$ can be obtained from $|0\rangle \otimes n$ by CNOT, Hadamard, phase, and measurement gates only. (iii) $|\psi\rangle$ is stabilized by exactly 2n Pauli operators. (iv) $|\psi\rangle$ is uniquely determined by $S(|\psi\rangle) =$ $Stab(|\psi\rangle) \cap P_n$ or the group of Pauli operators that stabilize $|\psi\rangle$

(x_{11}		x_{1n}	z_{11}		z_{1n}	r_1	
	:		•	• •		•	•	
	x_{n1}	•••	x_{nn}	z_{n1}	•••	z_{nn}	r_n	
	$x_{(n+1)1}$		$x_{(n+1)n}$	$z_{(n+1)1}$		$z_{(n+1)n}$	r_{n+1}	
	:		:	• •		:	•	
$\left(\right)$	$\overline{x_{(2n)1}}$		$x_{(2n)n}$	$z_{(2n)1}$		$\overline{z_{(2n)n}}$	r_{2n}	/

Instead of operating on the whole state vector, the state is represented by a much more compressed *tableau*.

It still requires vectorized operations on the boolean entries, that can be optimized in a similar fashion to the general state vector approach.

Clifford

Benchmarks



Clifford

Benchmarks



Optimized for observables.





Optimized for observables.





Optimized for observables.





Optimized for observables.





beyond opt_einsum

Approximation

Based on singular value decomposition (SVD).



A very frequent matrix product state (MPS).

But also other ansatzes are used.

Workload distribution



for q in range(nq):
 c.apply_gate('H', q)

```
for q in range(0, nq, 2):
    c.apply_gate('CNOT', q, q + 1)
```

c.apply_gate('CNOT', 4, 7)
c.apply_gate('CNOT', 4, 1)
c.apply_gate('CNOT', 4, 0)

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QiboTN



QiboTN



Transpilation

-- the bridge to hardware





Qibolab [arXiv: 2308.06313]

Quantum control

Execution flow



Qibolab - Interface



The **input** for a computation could be very standard, at the level of a **circuit**. That kind of interface is already defined by <u>Qibo</u> itself.

However, at a lower level, **pulses** are still a standard-enough way to interact with hardware, and these are defined by <u>Qibolab</u>.



def create():

instrument = DummyInstrument("myinstr", "0.0.0.0:0")

```
channels = ChannelMap()
channels ⊨ Channel(
    "readout",
    port=instrument.ports("o1")
)
```

```
•••
```

return Platform(
 "myplatform",
 qubits={qubit.name: qubit},
 instruments={instrument.name: instrument},
...

Qibolab - Drivers



		move nop	1,R0	Start at marker output channel 0 (move 1 Wait a cycle for R0 to be available.	into R0)
 Qblox Zurich QM 	loop:	set_mrk upd_param asl nop jlt	RØ 1000 R0,1,R0 R0,16,@loop	Set marker output channels to R0 Update marker output channels and wait 1 Move to next marker output channel (left Wait a cycle for R0 to be available. Loop until all 4 marker output channels	μs. -shift R0). have been set once.
		set_mrk upd_param stop		Reset marker output channels. Update marker output channels. Stop sequencer.	by Qblox

Qibosoq - Server on QICK [arXiv: 2310.05851]



Qibolab handles the whole connection, and takes care of fetching the single or multiple results.

For the single open source platform ^{FPGA FIRMWARE} currently in Qibolab, there has been a dedicate effort to define a suitable server, to optimize the communication with the board.

→ Qibosoq



Platform dashboard





Oibocal [arXiv: 2303.10397]

A due mention





- characterize the hardware
- calibrate control
- validate performances



Pulses' calibration



Scan spectrum to identify the coupled resonator frequency.





Tune the amplitude (duration) of the drive pulse, in order to excite the qubit from the ground state up to state $|1\rangle$.



Protocols report

QPU control implementation

$\mathsf{CHSH} \rightarrow$

Randomized benchmarking \checkmark

They are two of the routines available in Qibocal, allowing to validate the QPU performances.





Protocols report

QPU control implementation

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<u>test_q</u>	ubit_spec_tii1qs	2024-02-13	/home/users/ andrea.pasquale/ qibolab_platforms_qrc/ tii1qs_xld1000	06:59:45	06:59:50	-	andrea.pasquale
web_c	alibration_report_20240209_16	<u>3420</u> 2024-02-09	/home/users/qibocal/ webapp/ qibolab_platforms_qrc/ iqm5q	12:34:25	12:34:51	web_calibration	qibocal
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web_c	alibration_report_20240209_16	<u>3420</u> 2024-02-09	/home/users/qibocal/ webapp/ qibolab_platforms_qrc/ iqm5q	12:34:25	12:34:51	web_calibration	qibocal
Qibocal Reports



Calibrated data





\geq Not a one-man show...



Thanks